

## CLAIMS

1. A system for testing a semiconductor device comprising:  
a circuit board comprising circuitry adapted to provide an actual operating  
environment for the semiconductor device, the circuit board having a front side and a back  
5 side; and  
test terminals formed on the back side of the circuit board and arranged to couple the  
semiconductor device to the circuit board.
2. The system of claim 1, wherein the test terminals are pins protruding from the  
10 back side of the circuit board.
3. The system of claim 1, wherein the test terminals are arranged to correspond  
to the terminals of a component on the front side of the board.
- 15 4. The system of claim 1, further comprising a connector coupled to the test  
terminals.
5. The system of claim 1, further comprising an interface board coupled to the  
test terminals.
- 20 6. The system of claim 5, wherein the interface board is adapted to reverse the  
arrangement of the test terminals.
7. The system of claim 5, wherein the interface board is adapted to create a test  
25 environment that is the same as actual operating conditions for the semiconductor device.
8. The system of claim 5, further including a socket mounted on the interface  
board for coupling the semiconductor device to the circuit board.
- 30 9. The system of claim 8, wherein the socket is a socket for a module.
10. The system of claim 9 wherein the interface board is adapted to compensate  
for environmental differences caused the socket.

11. The system of claim 5, further comprising a support disposed between the interface board and the circuit board.

12. The system of claim 5, further comprising a fastener for attaching the interface  
5 board to the circuit board.

13. The system of claim 5, further comprising a connector coupled between the interface board and the test terminals.

10 14. The system of claim 5, wherein the interface board is adapted to reverse the arrangement of the test terminals, and further comprising a socket for a module coupled to the interface board.

15 15. A system for testing a semiconductor device comprising:  
a circuit board comprising circuitry adapted to provide an actual operating environment for the semiconductor device, the circuit board having a front side and a back side; and  
means for coupling the semiconductor device to the back side of the circuit board.

20 16. The system of claim 15, wherein the means for coupling the semiconductor device to the back side of the circuit board comprises test terminals formed on the back side of the circuit board and arranged to couple the semiconductor device to the circuit board.

25 17. The system of claim 16, wherein the test terminals are pins protruding from the back side of the circuit board.

18. The system of claim 15, wherein the means for coupling the semiconductor device to the back side of the circuit board comprises a connector.

30 19. The system of claim 15, wherein the means for coupling the semiconductor device to the back side of the circuit board comprises an interface board coupled to the circuit board.

20. The system of claim 15, wherein the means for coupling the semiconductor device to the back side of the circuit board further comprises a socket mounted on the interface board for coupling the semiconductor device to the circuit board.

21. The system of claim 20, wherein the socket is a socket for a module.

22. A method for testing a semiconductor device comprising:  
coupling the semiconductor device to the back side of a circuit board comprising  
circuitry adapted to provide an actual operating environment for the semiconductor device;  
and  
operating the circuitry on the circuit board.

23. The method of claim 22 wherein coupling the semiconductor device to the back side of the circuit board comprises:  
coupling an interface board to the back side the circuit board; and  
coupling the semiconductor device to the interface board.

24. The method of claim 23 wherein coupling the semiconductor device to the interface board comprises coupling the semiconductor device to a socket on the interface board.

25. The method of claim 23 wherein coupling the semiconductor device to the interface board comprises coupling a module having the semiconductor device mounted thereon to a socket on the interface board.